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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Sunil Kumar Sharma
Title: MAPPING OF PROGRAMMABLE LOGIC DEVICES
Serial No.: 10/675,908
Filing Date: September 29, 2003
Attorney Dkt. No.: 11556(P-54)USA; 2110-80-3

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I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail and is addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 29th day of August, 2006.


Signature

SUBMISSION OF FORMAL DRAWINGS

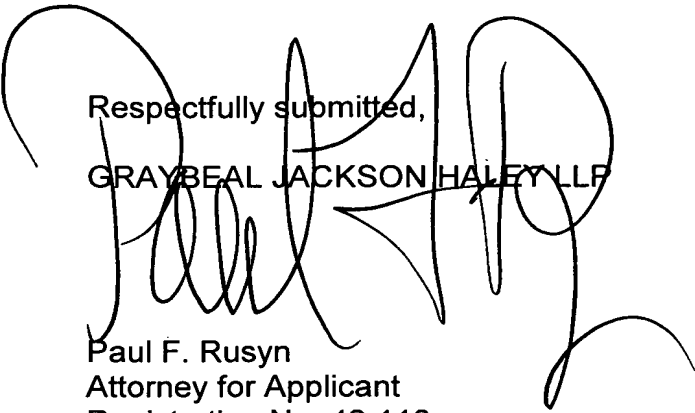
August 29, 2006

Attn: OFFICIAL DRAFTSMAN

COMMISSIONER FOR PATENTS:

In response to the Notice Regarding Drawings mailed June 29, 2006, requesting corrected, formal drawings for the above-identified application, Applicant hereby submits the enclosed drawings, including Figures 1 – 8, for approval and entry.

Please contact Applicant's attorney at the phone number shown below should there be any questions concerning the enclosed formal drawings.



Respectfully submitted,

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